

RESPONSE TO NOTICE OF DRAWING INCONSISTENCY

Serial Number: 09/945,500

Filing Date: August 30, 2001

Title: PROGRAMMABLE MEMORY ADDRESS AND DECODE CIRCUITS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS

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Dkt: 1303.029US1

REMARKS

This paper is in response to the Notice of Drawing Inconsistency With Specification mailed October 14, 2005, a copy of which is enclosed. Applicant herein amends the specification to conform to the drawings originally filed with this application. Specifically, the brief description now refers to Figure 6D. No new matter is believed proposed by this amendment. Applicant further corrects a grammatical error. Applicant further notes that prosecution of this application was reopened and a Request for Continued Examination was filed 14 October 2005.

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 349-9587 to facilitate prosecution of this application. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

LEONARD FORBES

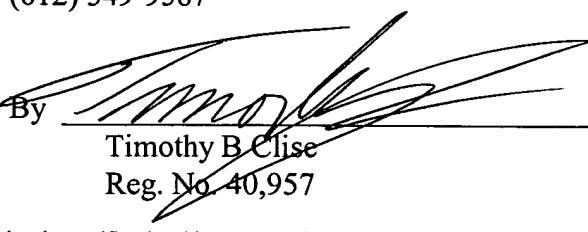
By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 349-9587

Date

14 Nov '05

By


Timothy B. Clise
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 14 day of November, 2005.

Name

KACIA LEE

Signature

